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PACKAGE STRUCTURE OF SOLID-STATE IMAGE SENSOR

BACKGROUND OF THE INVENTION

5 The present invention relates to a package structure for a color solid-state image sensor to which a color filter is applied.

Fig. 1 is a cross-sectional view showing a prior art package structure of a solid-state image sensor.

A ceramic vessel 1 has a cavity 2 for receiving a solid-state image sensor chip 3. The chip 3 is fixed to the bottom surface of the cavity 2 by a bonding paste 4. The chip 3 has a light receiving surface on which a plurality of light receiving pixels are arranged in a matrix-like manner. A color filter, which associates each of the light receiving pixels with a predetermined color component, is stuck on the light receiving surface to enable color imaging. A plurality of leads are embedded in the vessel 1. One end 6 of each lead is exposed in the cavity 2, while the other end of each lead end 6 exposed at the outer surface of the vessel 1. Each lead end 6 exposed in the cavity 2 is connected to the chip 3 by a wire 5. A transparent glass plate 7 seals the chip 3 in the vessel 1.

The color filter is an organic resin film that covers each light receiving pixel of the chip 3. The resin film is stuck on the surface of the chip 3 and then, in correspondence with each pixel, colored to a predetermined color. When exposed to light, such as sunlight, over a long period of time, the color filter may discolor or fade. This restricts the environment in which the solid-state image

sensor may be used.

SUMMARY OF THE INVENTION

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It is an object of the present invention to provide a package structure of a solid-state image sensor that prevents discoloring or fading of the color filter.

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To achieve the above object, the present invention provides a package structure of a solid-state image sensor having a solid-state image sensor chip and a color filter stuck on a light receiving surface of the chip. The package structure includes a vessel for packaging the solid-state image sensor chip. An optically transparent protection plate is attached to the vessel to cover the light receiving surface of the chip. A resin layer is arranged between the chip and the protection plate to absorb light having a predetermined wavelength.

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A further aspect of the present invention provides a solid-state image sensor including a solid state image sensor chip, which has a light receiving surface and a color filter stuck on the light receiving surface. The device also has a vessel including a cavity for receiving the chip. A resin layer is arranged in the cavity to cover the color filter. The resin layer absorbs light having a predetermined wavelength. A protection plate is attached to the vessel to cover at least part of the resin layer.

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Other aspects and advantages of the present invention will become apparent from the following description, taken in conjunction with the accompanying drawings, illustrating by way of example the principles of the invention.

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BRIEF DESCRIPTION OF THE DRAWINGS

The invention, together with objects and advantages thereof, may best be understood by reference to the following description of the presently preferred embodiments together with the accompanying drawings in which:

Fig. 1 is a schematic cross-sectional view showing a prior art package structure of a solid-state image sensor;

Fig. 2 is a schematic cross-sectional view showing a package structure of a solid-state image sensor according to a preferred embodiment of the present invention;

Fig. 3 is a graph showing the relationship between the wavelength of a light irradiated against a chip and the transmittance; and

Fig. 4 is an exploded perspective view showing the package structure of Fig. 2.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

A package structure of a solid-state image sensor according to a preferred embodiment of the present invention will now be described with reference to Figs. 2 to 4. A solid-state image sensor chip 13, to which a color filter is applied, is fixed to a vessel 11 at the bottom surface of a cavity 12 by a bonding paste 14. The vessel 11 and the chip 13 are respectively identical to the conventional vessel 1 and chip 3 shown in Fig. 1. Lead ends 16a are connected to the chip 13 with wires 15.

A resin layer 17, which mainly absorbs ultraviolet light and transmits visible light, is formed in the cavity 12. More specifically, a transparent resin is put in the cavity 12 with the chip 13. A glass plate 18 is then

attached to the vessel 11 to cover the resin. This forms the transparent resin layer 17 between the chip 13 and the glass plate 18.

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The resin layer 17 absorbs light having a shorter wavelength, such as ultraviolet light, and transmits light in the visible light band. More specifically, the preferred material is one that transmits light having a wavelength of 380nm to 780nm and absorbs light having a wavelength of 380nm or less. The preferred embodiment employs an acrylic transparent resin manufactured by Kyouritsu Kagaku Kabushiki Kaisha (product number XLV-14SG2) as the transparent resin.

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Fig. 3 is a graph illustrating the wavelength of the light that reaches the light receiving surface of the chip 13. In the graph, curve a represents the transmittance of light in the preferred embodiment, which uses the resin layer 17, and curve b represents the transmittance of light in the prior art device. The glass plate 18 and the resin layer 17 each have a thickness of about one millimeter.

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In the preferred embodiment (curve a), light having a wavelength shorter than visible light (wavelength being 380nm or less) is effectively absorbed by the resin layer 17 and substantially does not reach the light receiving surface of the chip 13. In the prior art (curve b) in which the resin layer 17 is not employed, light having a wavelength of 380nm or less, and especially, 350nm or less, reaches the light receiving surface of the chip 3 at almost the same level as visible light. Accordingly, the effectiveness of the resin layer 17 to filter light is apparent.

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As shown in Fig. 4, the length of the glass plate 18 is

greater than that of the cavity 12 and the width of the glass plate 18 is less than that of the cavity 12. Thus, when the glass plate 18 is attached to the vessel 11, a gap is formed between the glass plate 18 and the cavity 12 on each side of the glass plate 18. The gaps serve to decrease internal stress that is produced when the resin layer 17 contracts or expands. The width of the glass plate 18 is at least wider than the light receiving surface of the chip 13.

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Except for the glass plate 18, the package structure of the preferred embodiment is substantially the same as that of the prior art shown in Fig. 1. This enables the package structure of the preferred embodiment to be handled in the same manner as the conventional solid-state image sensor.

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The color filter is protected in the solid-state image sensor of the preferred embodiment. Thus, the solid-state image sensor of the preferred embodiment may be used in a wide variety of environments.

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With reference to Fig. 4, a method for fabricating the package structure of Fig. 2 will now be discussed.

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The vessel 11 accommodates the chip 13 in the cavity 12. A plurality of leads are embedded in the vessel 11. The end 16a of each lead is exposed in the cavity 12. The other end 16b of each lead is exposed at the outer surface of the vessel 11 to serve as a terminal connected to an external circuit.

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The chip 13, which is fabricated through a predetermined fabrication process, is fixed to the bottom surface of the cavity 12 by the bonding paste 14. Then, a

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plurality of bonding pads (not shown) formed at the ends of the chip 13 and the lead ends 16a exposed in the cavity 12 are electrically connected by wires 15 (e.g., gold wires). Next, a fluxional transparent resin is charged into the cavity 12. The amount of transparent resin is controlled such that the resin covers the chip 13 and the wires 15 but does not flow out of the cavity 12. Before the resin hardens, the surface of the resin, or the cavity 12, is covered by the glass plate 18. The width of the glass plate 18 is less than that of the cavity 12, and the length of the glass plate 18 is greater than that of the cavity 12. Accordingly, the glass plate 18 is arranged so that a gap is formed between the sides of the glass plate 18 and the opposing walls of the vessel 11. The transparent resin in the cavity 12 is then hardened to form the resin layer 17. The package structure of Fig. 2 that protects the color filter of the chip 13 is obtained in this manner.

When the resin is hardened, contraction of the resin may produce internal stress. However, in such case, the resin located at the gaps is drawn inward thereby absorbing the stress. When thermal changes expand the resin layer 17, the resin expands out of the cavity 12 through the gaps. This prevents the application of excessive stress to the vessel 11 and the glass plate 18.

The preferred embodiment has the advantages described below.

Light having a relatively short wavelength (ultraviolet light) that causes deterioration of the color filter is absorbed by the resin layer 17. Thus, such light does not reach the color filter. As a result, the color filter is

protected from light that causes deterioration, and discoloring of the color filter is prevented. A solid-state image sensor having superior durability is therefore obtained.

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Since the resin sealing the chip absorbs undesirable light, a filter for exclusively cutting out light of certain wavelengths is not needed. This decreases the fabrication cost of the solid-state image sensor and enables an inexpensive solid-state image sensor to be obtained.

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In the preferred embodiment, a substantially scratchproof plate that allows the transmission of light (e.g., acrylic resin) may be used in lieu of the glass plate 18.

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It should be apparent to those skilled in the art that the present invention may be embodied in many other specific forms without departing from the spirit or scope of the invention. Therefore, the present examples and embodiments are to be considered as illustrative and not restrictive, and the invention is not to be limited to the details given herein, but may be modified within the scope and equivalence of the appended claims.